

FORM PTO/SB/08A/B (10-01) Substitute for PTO-1449A/B		Attorney Docket Number	50470/DBP/B600
<p style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)</p>		Application Number	09/892,310
		Filing Date	June 26, 2001
		Applicant(s)	Zheng Qi, et al.
		Group Art Unit	2131
		Examiner Name	not yet assigned

U.S. PATENT DOCUMENTS				
EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE RECEIVED
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FOREIGN PATENT DOCUMENTS				
EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
3S	EP	0 661 843 A2	07-05-1995	Alvarez
3S	EP	0 661 843 A3	11-10-1999	Alvarez

OTHER DOCUMENTS		
EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
3S	/	European Search Report dated January 9, 2003 from corresponding European Patent Application No. 01309323.2-1525, filed November 2, 2001
3S	/	JENKINS, et al.; <i>I.C. multiplexer increases analogue switching speeds</i> ; Electronic Engineering; Vol. 45, No. 540; February 1973; pp. 73-75; XP-000807191
3S	/	MCLOONE et al.; <i>A High Performance FPGA Implementation of DES</i> ; 2000 IEEE Workshop On Signal Processing Systems, SIPS 2000. Design and Implementation (CAT. NO. 00TH8528), October 2000; pp. 374-382 XP-002251022

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EXAMINER SIGNATURE	<i>Shawn</i> <i>Alvar</i>	DATE CONSIDERED	9/20/04, 9/20/04
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